

SYSTEM AND METHOD FOR COMPARISON AND COMPENSATION OF DELAY
VARIATIONS BETWEEN FINE DELAY AND COARSE DELAY CIRCUITS

ABSTRACT OF THE DISCLOSURE

Embodiments of the present invention are directed to compensating for variations in time delay adjustments of synchronizing circuits that synchronize an external clock signal applied to an integrated circuit with internal clock signals generated in the integrated circuit in response to the external clock signal. Compensation is provided by adjusting the time delay relationship between fine and coarse delay circuits of an adjustable delay circuit to compensate for variations from an expected time delay relationship.